Amendments to the Specification:

Please replace paragraph [0020] with the following amended paragraph:

[0019]   FIG. 1 illustrates an inkjet printhead 10 according to one embodiment of the present invention. The printhead 10 includes a housing 12 that defines a nosepiece 13 and an ink reservoir 14 containing ink or a foam insert saturated with ink. The housing 12 can be constructed of a variety of materials including, without limitation, at least one of polymers, metals, ceramics, composites, and the like. The inkjet printhead 10 illustrated in FIG. 1 has been inverted to illustrate a nozzle portion 15 of the printhead 10. The nozzle portion 15 is located at least partially on a bottom surface 11 of the nosepiece 13 for transferring ink from the ink reservoir 14 onto a printing medium. The nozzle portion 15 can include a heater chip 16 (not visible in FIG. 1) and a nozzle plate 20 having a plurality of nozzles 22 that define a nozzle arrangement and from which ink drops are ejected onto printing medium that is advanced through a printer (not shown). The nozzles 22 can have any cross-sectional shape desired including, without limitation, circular, elliptical, square, rectangular, and any other polygonal shape that allows ink to be transferred from the printhead 10 to a printing medium. The heater chip 16 can be formed of a variety of materials including, without limitation, various forms of doped or non-doped silicon, doped or non-doped germanium, or any other semiconducting material. The heater chip 16 is positioned to be in electrical communication with conductive traces 17 provided on an underside of a tape member 18. The tape member 18 is coupled to one side 24 of the housing 12 and most of an underside 26 of the nosepiece 13. Each conductive trace 17 connects at one end to a contact pad, bond or wire 23 adjacent the nozzle portion 15 and terminates at an opposite end at a contact pad 28.

Please replace the Abstract with the following amended Abstract:

A programmable memory on an inkjet printhead chip. The memory ~~comprises~~includes at least a memory array that has a plurality of memory elements, and a bipolar device that isolates a memory element from another memory element in the memory array.